Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9703	block adj (encod$4 or cod$4)	USPAT; DERWENT	OR	ON	2005/05/31 08:39
L2	379	1 and (m adj bits) and (n adj bits)	USPAT; DERWENT	OR	ON	2005/05/31 08:39
L3	113	2 and ("1" adj bit) and ("0" adj bit)	USPAT; DERWENT	OR	ON	2005/05/31 08:40
L4	17	3 and ("2N-1")	USPAT; DERWENT	OR	ON	2005/05/31 08:40
L5	2850	block adj (encoding or coding)	USPAT; DERWENT	OR	ON	2005/05/31 08:39
L6	134	5 and (m adj bits) and (n adj bits)	USPAT; DERWENT	OR	ON	2005/05/31 08:39
L7	32	6 and ("1" adj bit) and ("0" adj bit)	USPAT; DERWENT	OR	ON	2005/05/31 08:40
L8	0	7 and ("2N-1")	USPAT; DERWENT	OR	ON	2005/05/31 08:40
L9	0	7 and (balance adj coding adj block)	USPAT; DERWENT	OR	ON	2005/05/31 08:40

	U	1	Document ID	Issue Date	Pages
1			US 6799246 B1	20040928	241
2			US 6217234 B1	20010417	237
3			US 6141721 A	20001031	334
4			US 6119213 A	20000912	234
5			US 5995727 A	19991130	237
6			US 5984512 A	19991116	236
7			US 5963154 A	19991005	235
8			US 5878273 A	19990302	236

	Title	Current OR	Current XRef
1	Memory interface for reading/writing data from/to a memory	711/117	710/53; 711/217; 711/218; 711/219
2	Apparatus and method for processing data with an arithmetic unit	709/247	
3	Method of asynchronous memory access	711/1	365/189.04; 365/230.02; 365/230.06; 711/105; 711/167; 711/168
4	Method for addressing data having variable data width using a fixed number of bits for address and width defining fields	711/202	711/203; 711/212; 711/220
5	Video decompression	709/247	
6	Method for storing video information	709/219	715/716
7	Technique for decoding variable and fixed length codes	341/67	
8	System for microprogrammable state machine in video parser disabling portion of processing stages responsive to sequence.sub.-- end token generating by token generator responsive to received data	710/5	712/220

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1		Wise; Adrian P. et al.	X						
2		Dewar; Kevin D. et al.	X						
3		Patterson; Donald William et al.	X						
4		Robbins; William P.	X						
5		Wise; Adrian P. et al.	X						
6		Jones; Anthony Mark et al.	X						
7		Wise; Adrian P. et al.	X						
8		Wise; Adrian P. et al.	X						

	Image Doc. Displayed	PT
1	US 6799246	
2	US 6217234	
3	US 6141721	
4	US 6119213	
5	US 5995727	
6	US 5984512	
7	US 5963154	
8	US 5878273	

	U	1	Document ID	Issue Date	Pages
9			US 5835792 A	19981110	240
10			US 5829007 A	19981027	236
11			US 5828907 A	19981027	242
12			US 5821885 A	19981013	236
13			US 5801973 A	19980901	236
14			US 5798719 A	19980825	240
15			US 5768629 A	19980616	240
16			US 5740460 A	19980414	241
17			US 5703793 A	19971230	239

	Title	Current OR	Current XRef
9	Token-based adaptive video processing arrangement	710/68	345/506; 712/36
10	Technique for implementing a swing buffer in a memory array	711/5	345/567; 345/571; 365/189.04; 365/230.04; 365/230.05; 711/104; 711/168
11	Token-based adaptive video processing arrangement	710/68	345/506; 712/36
12	Video decompression	341/67	
13	Video decompression	708/203	
14	Parallel Huffman decoder	341/67	341/65; 704/200.1
15	Token-based adaptive video processing arrangement	710/68	345/506; 712/36
16	Arrangement for processing packetized data	348/473	
17	Video decompression	382/232	708/203

	Retrieval Classif	Inventor	S	C	P	2	3	4	5
9		Wise; Adrian P. et al.	X						
10		Wise; Adrian P. et al.	X						
11		Wise; Adrian P. et al.	X						
12		Wise; Adrian P. et al.	X						
13		Wise; Adrian P. et al.	X						
14		Wise; Adrian P. et al.	X						
15		Wise; Adrian P. et al.	X						
16		Wise; Adrian P. et al.	X						
17		Wise; Adrian P. et al.	X						

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